EPC Experimental Processors Corp. is investigating the prospect of putting the SEP into full production. However before they do this, they want to verify that the processor actually works in hardware.

To this end, they want to do a test implementation of the SEP on a Field Programmable Gate Array (FPGA).

Your job, if you choose to accept the challenge is to implement the SEP on an FPGA. You should also write a report and give a presentation that gives specific details of your implementation.